

BOX PCT
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UNDER THE PATENT COOPERATION TREATY--CHAPTER II

APPLICANT(S): Thomas STURM
ATTORNEY DOCKET NO.: P01,0140
INTERNATIONAL APPLICATION NO: PCT/DE99/03824
INTERNATIONAL FILING DATE: 01 December 1999
INVENTION: SOFT DECISION DECODING OF A SCHEDULED
CONVOLUTIONAL CODE

Assistant Commissioner for Patents,
Washington D.C. 20231

INFORMATION DISCLOSURE STATEMENT

According to 37 C.F.R. 1.97(b)

Sir:

In accordance with the provisions of 37 C.F.R. 1.56 and the requirements of 37 C.F.R. 1.98, Applicant respectfully requests that a citation and examination of the references identified on the attached PTO 1449 form be made during the course of examination of the above-identified application for United States Patent.

The present Information Disclosure Statement is being filed according to 37 C.F.R. 1.97(b) and before the latter occurrence of:

- (1) three months from the filing date of a national application;
- (2) three months from the date of entry of the national stage as set forth in 37 C.F.R. 1.491 in an international application; or
- (3) the mailing date of a first Office Action on the merits.

REMARKS

The attached PTO 1449 form lists related art references for the above identified application, including those identified in the International Search Report, copy of which is enclosed herewith.


EXPLANATION OF RELEVANCE

References AJ and AQ were cited in the International Search Report. References AA-AC and AK-AL represent additional related art. References AA-AC and AQ are in English, so no further commentary on their teaching is required. References AJ-AL are foreign language patents. An English translation of the abstract has been provided, and a full translation will be produced at the request of the Examiner.

The filing of the present Information Disclosure Statement is not to be construed as a representation that a search has been made, and is not to be construed as an admission that the information cited in the present Information Disclosure Statement is, or is considered to be, material to patentability as defined in 37 C.F.R. 1.56(b).

The above citation of related art is not a representation that such art constitutes a complete or exhaustive listing of all pertinent related art, nor that it necessarily includes the closest or most relevant art. The aforementioned citation comprises a voluntary citation of related art of which applicant and his attorney are presently aware and is not intended to serve as a substitute for the Examiners own search.

Submitted by,

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Form P.T.O. 1449 U.S. Department of Commerce
Patent and Trademark Office

Docket No.:
P01,0140

Serial No.
097856936
New Application

LIST OF RELATED ART CITED BY APPLICANT
(use several sheets if necessary)

Applicant(s):
Thomas STURM

Filing Date
Herewith

Group Art Unit
Unknown

U.S. PATENT DOCUMENTS

Examiner's Initials		Document Number	Date	Name	Class	Subclass	Filing Date If appropriate
	AA	5,574,751	November 12, 1996	Trelewicz			
	AB	5,566,191	October 15, 1996	Ohnishi et al			
	AC	5,721,746	February 24, 1998	Hladik et al			
	AD						
	AE						
	AF						
	AG						
	AH						
	AI						

FOREIGN PATENT DOCUMENTS

		Document Number	Date	Country	Class	Subclass	Translation	
							Yes	No
	AJ	EP 0 391 354 A2	10 October 1990	Europe				
	AK	DE 44 37 984 A1	14 August 1996	Germany				
	AL	DE 40 38 251 A1	04 June 1992	Germany				
	AM							
	AN							
	AO							
	AP							

OTHER RELATED ART (Including Author, Title, Date, Pertinent Pages, Etc.)

	AQ	JOERESSEN, Olaf J, et al., "HIGH-SPEED VLSI ARCHITECTURES FOR SOFT-OUTPUT VITERBI DECODING", 8367 Journal of VLSI Signal Processing (1994), No. 2, Dordrecht, NL, pages 169-180
	AR	
	AS	
	AT	
	AU	
	AV	
	AW	
	AX	
	AY	

Examiner

Date Considered

*EXAMINER: Initial if reference considered, whether or not citation is in conformance with MPEP 609. Draw line through citation if not in conformance and not considered. Include copy of this form with next communication to applicant.